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PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

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**APPLICANTS: Goldman Stanley;	
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**CONTINUING DATA VERIFIED:	
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** FOREIGN APPLICATIONS VERIFIED:	
PG-PUB DO NOT PUBLISH RESCIND ATTORNEY DOCKET NO	
Foreign ; liority claimed	
Verified and Acknowledged Examiners's initials	
TITLE: Fully integrated low noise multi-loop synthesizer with fine frequency resolution for HD channel and RF wireless local oscillator applications	Į)
U.S.DEPT. OF COMM.PAT.A.TM-PTO.	13GL(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED		
<u> </u>		Assistant Examiner	Total Claims	Pri: 0.0	nt Claim for
ISSUE FEE			DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.
	-	Primary Examiner	#	· 7	
TERMINAL		PREPARED FOR ISSUE	Application Examiner		
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